

80V , 1.2A ,480KHz DCDC,Step-Down Converter

SSP9480

General Description

The SSP9480 is a monolithic, step-down, switch-mode converter with a built-in power MOSFET. Current-mode operation provides a fast transient response and eases loop stabilization.

The wide input Voltage range (4.5V to 80V) provides high efficiency output of 1.2A current. Low shutdown mode quiescent current of 0.1 μ A is suitable for battery-powered applications.

Fail-state protection includes cycle-by-cycle current-limiting protection and thermal shutdown protection.



Features

- Input Voltage Range:4.5~80V
- Output 1.2A persistent current, 1.8A peak current
- >92% efficiency
- Low shutdown mode current: <1 μ A
- Output from 1V to 0.95 $\times$ V_{in} adjustable
- 480KHz fixed switching frequency
- 500m Ω internal power MOSFET
- Cycle-by-cycle current-limiting protection
- Short circuit protection
- Thermal shutdown protection
- ESOP-8 package

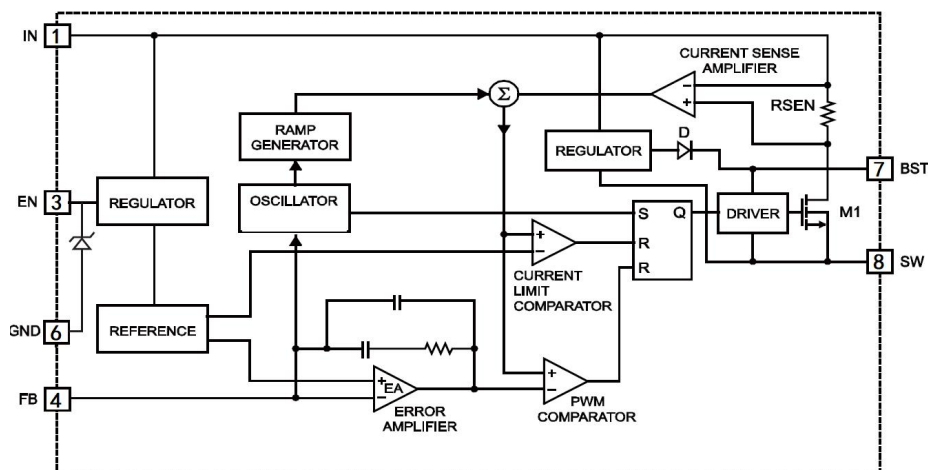
Applications

- High voltage power conversion
- Industrial power system
- Distributed power system
- Battery powered system

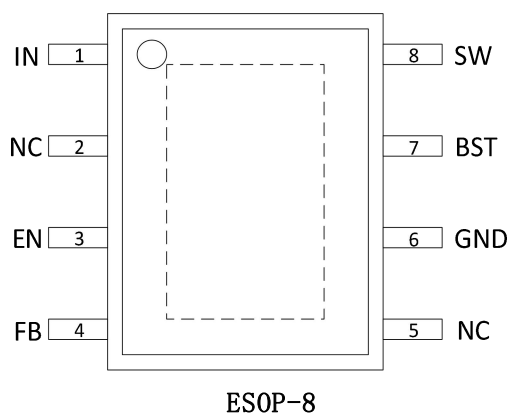
Order specification

Part No	Package	Manner of Packing	Devices per bag/reel
SSP9480	ESOP-8	Reel	4000

Block Diagram and Pin Arrangement Diagram



Pin Assignment



Pin No.	Pin Name	Description
1	IN	Input Supply. All internal control circuits are powered, including switching tubes. A decoupling capacitor to ground is required close to this pin to reduce switching spikes.
2	NC	No connected.
3	EN	Enable input. Pull this pin below the specified threshold to disable. Pull it above the specified threshold to enable. Connect 100K resistor to IN, it can be turned on automatically.
4	FB	Feedback. This is the input to the error amplifier. Set the output voltage. When the load is short circuited and the FB voltage is

		below 250mV, the foldback circuit will reduce the oscillation frequency to ensure reliable current limiting protection.
5	NC	No connected.
6	GND	Ground. It should be connected as close as possible to the output capacitor, avoid high current switching paths.
7	BST	Bootstrap. This is the positive power supply for the internal floating high side MOSFET driver. Connect a bypass capacitor between this pin and SW pin.
8	SW	Switch node. A low VF Schottky diode to ground is required close to this pin to reduce switching spikes.

Absolute Maximum Ratings

Parameter	Range
Supply Voltage (V_{IN})	-0.3V~85V
Switch Voltage (V_{SW})	-0.3V~ $V_{IN}(MAX)+0.3V$
BST to SW	-0.3V~6.0V
All Other Pins	-0.3V~6.0V
Junction Temperature	150°C
Lead Temperature	260°C
Storage Temperature	-65°C~150°C
Operating Junction Temp (T_J)	-40°C~125°C
Junction-to-Ambient Thermal Resistance (θ_{JA})	50°C/W
Junction-to-Case Thermal Resistance (θ_{JC})	10°C/W

Recommended Operating Conditions

Parameter	Range
Supply Voltage (V_{IN})	4.5V ~ 80V
Switch Voltage (V_{SW})	1V ~ 0.95* V_{IN}
Operating Temperature	-40°C ~ +85°C

Note 1: The maximum limit means that the chip may be damaged if it exceeds the working range. The recommended operating range refers to the range within which the device functions normally. Electrical parameters define the specifications for the DC and AC parameters of the device within its operating range and under test conditions that ensure specific performance indicators.

Electrical Characteristics

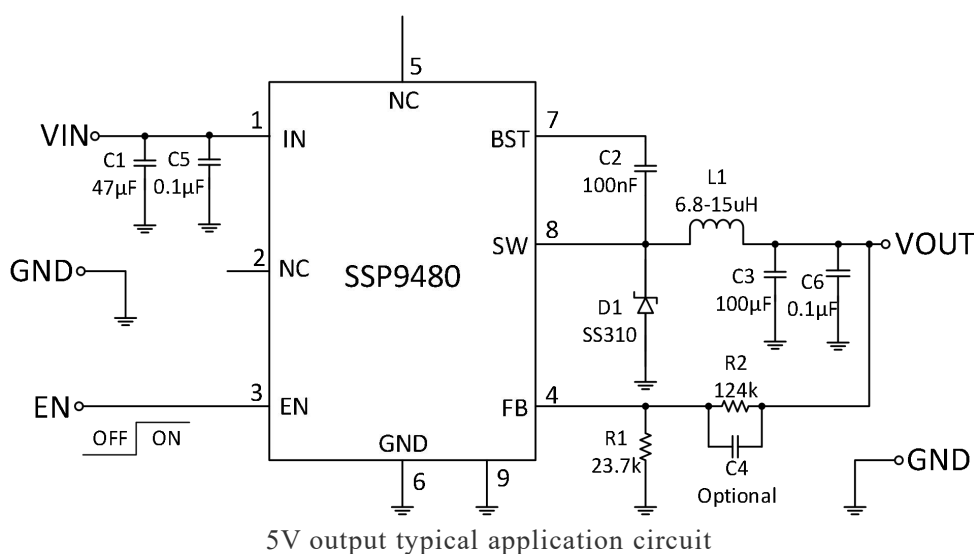
Vin=12V, Ta= 25°C, unless specified otherwise.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Feedback Voltage	Vfb	4.5<Vin<80V	0.792	0.812	0.832	V
Upper Switch ON Resistance	Rsw	Vbst-Vsw=5V		500		mΩ
Upper Switch Leakage	Iswleak	Ven=0V, Vsw=0V			1	μA
Limiting Current	Ilim			1.8		A
Oscillator Frequency	Fosc		380	480	580	KHz
Foldback Frequency	Fsw-f	Vfb=0V		150		KHz
UVLO Up Threshold	Vuvlo-r		2.9	3.3	3.73	V
UVLO Hysteresis	Vuvlo-f		2.65	3.05	3.45	V
Minimum Switch ON Time	Ton min			100		ns
Enable Threshold Voltage	Venr		1.4			V
Enable Turn-off Voltage	Venf				1.0	V
EN Input Current	Ien	Ven=2V		3		μA
		Ven=0V		0.1		μA
Quiescent Current	Isq	Ven=2V, Vfb=1V		0.61	0.86	mA
Shutdown Current	Isd	Ven=0V, 4.5V<VIN<70V		0.1	1.0	μA
Thermal Shutdown	Temp			240		°C

Detailed Description

The SSP9480 is a 480KHz, current mode step-down switching regulator with integrated internal high voltage power MOSFET. The circuit internal error amplifier output is proportional to the peak inductance current, the feedback signal is compared with the internal reference voltage of 0.812V, stable output voltage. It features wide input voltage range, and precision current limit. Low operational quiescent current feature is suitable for battery powered applications.

Application Circuits



PCB Layout Guidelines:

PCB layout is very important for the circuit to achieve stable operation. The following suggestions are for your reference:

1. Switching current path as short as possible, input capacitance, high-side MOSFET and external high-speed switching Schottky diode formed loop area as small as possible.
2. Bypass ceramic capacitor is placed near the VIN end. SW output routing should be as short and thick as possible.
3. All feedback circuit connections should be short and direct, with feedback resistance and compensation elements as close to the chip as possible.
4. SW route should be far away from sensitive simulated areas, such as FB.
5. SW, IN, and especially ground should be connected to a large copper-clad area. The chip belly radiator should be connected to the circuit board GND to cool the chip, improve thermal performance, and enhance long-term reliability.

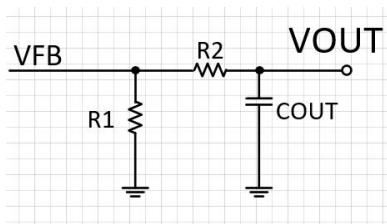
Select components

Setting the Output Voltage

The output voltage is set using a resistive voltage divider from the output voltage to FB pin.

$$V_{FB} = V_{OUT} \times R1 / (R1 + R2)$$

Typical voltage of VFB is 0.812V.



Reference resistance for each output voltage:

Vout (V)	R1(KΩ)	R2(KΩ)
1.8	64.9 (1%)	80.6 (1%)
2.5	23.7 (1%)	49.9 (1%)
3.3	16.2 (1%)	49.9 (1%)
5	23.7 (1%)	124 (1%)

Inductor

The inductor is required to supply constant current to the output load. A larger value inductor will result in lower output ripple voltage. Generally, a good rule for determining the inductance to use is to allow the peak-to-peak ripple current in the inductor to be approximately 30% of the maximum load current. Also, make sure that the peak inductor current is below the maximum switch current limit, it will not saturate at the maximum inductance peak.

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{V_{in} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductance ripple current. If the maximum load current is 2A, the inductor ripple current is selected to be about 30%. The maximum peak inductance current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

Under light load conditions up to 100mA, larger inductors are recommended to improve efficiency.

Input Capacitor

The input capacitor can be electrolytic, tantalum or ceramic. When using electrolytic or tantalum capacitors, a small, high quality ceramic capacitor, i.e. 0.1μF, should be placed as close to the IC as possible. When using ceramic capacitors, make sure they have sufficient capacitance values to prevent input from excessive voltage ripple.

Output Capacitor

The output capacitor is used to maintain the DC output voltage. Low ESR electrolytic capacitors are recommended to keep the output voltage ripple low. The characteristics of the output capacitor will affect the stability of the voltage stabilizer system.

Catch Diode

When the upper MOSFET is off, the output diode is inductance current freewheeling. Use Schottky diodes to reduce the loss caused by the diode's forward guide on voltage and reverse recovery. The average current flowing through the diode can be estimated by the following formula:

$$I_D = I_{OUT} \times \left(1 - \frac{V_{OUT}}{V_{IN}} \right)$$

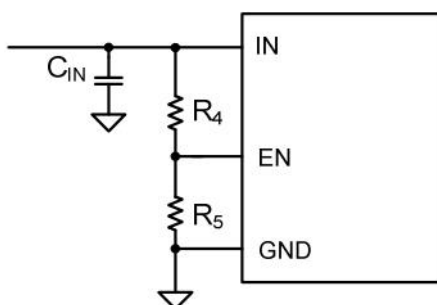
When selecting the diode, the rated voltage should be greater than the maximum input voltage, and the average current of the secondary tube should be greater than the maximum load current.

Enable-control

Pulling the EN pin voltage below the specified threshold will shut down the chip. In off mode, the SSP9480 shutdown current drops to less than 1μA. Pulling the EN pin high will open the chip. Connect the 100K resistor to the Vin pin to automatically start the chip, or you can control the starting voltage through the resistance voltage division.

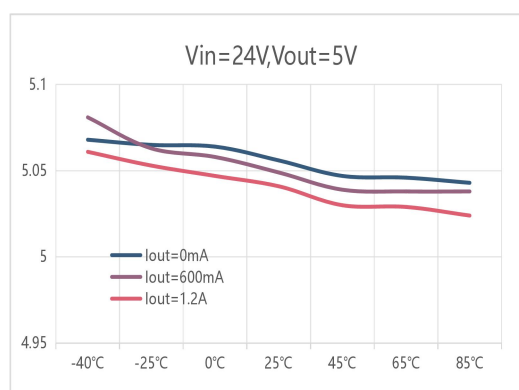
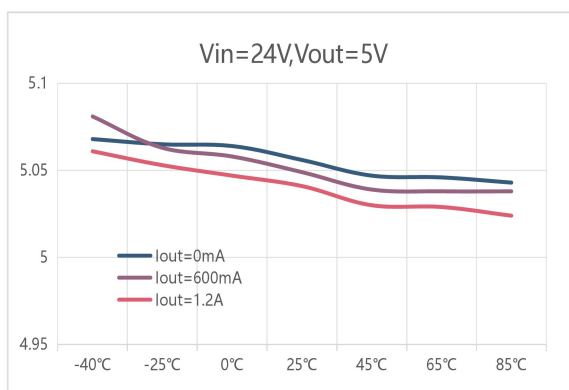
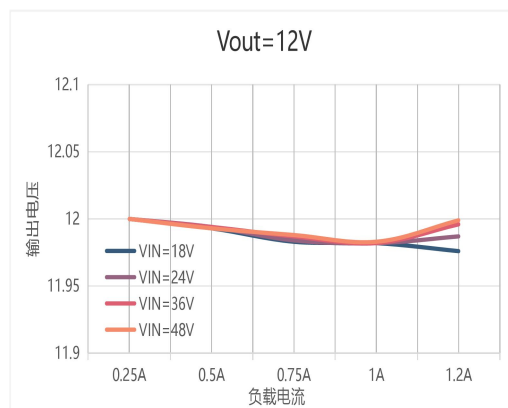
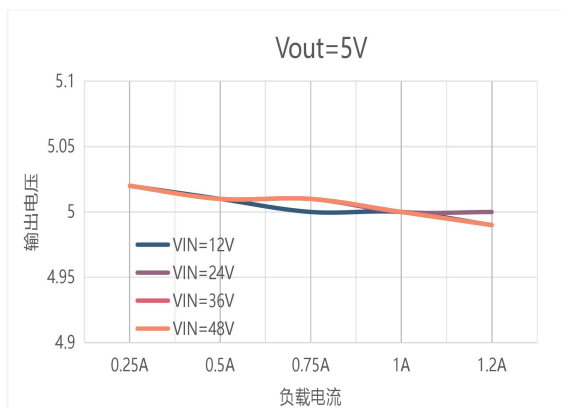
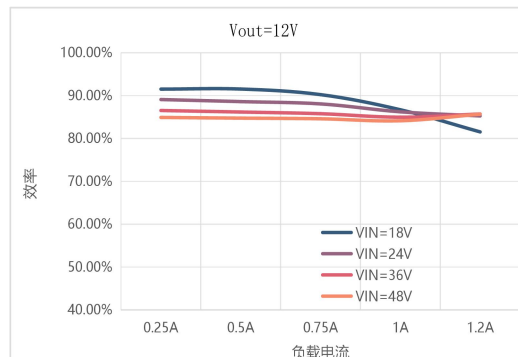
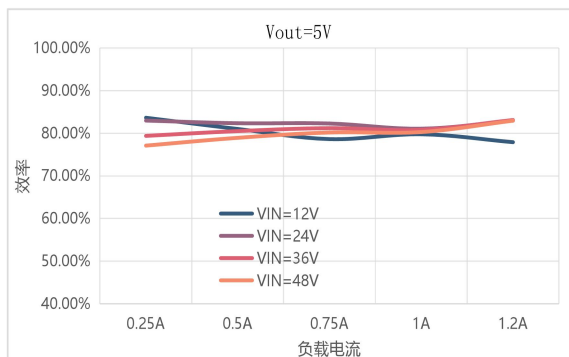
$$V_{IN} \times R_5 / (R_4 + R_5) > V_{ENR}$$

V_{ENR} is the ascending threshold voltage of EN, the lower limit of which is 1.4V.

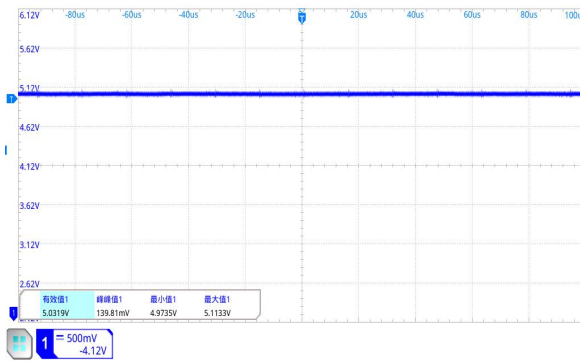


Typical Performance Characteristics

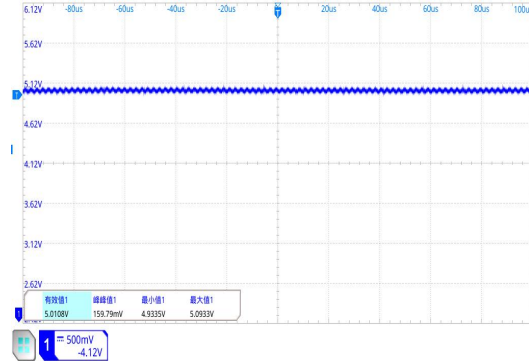
$C_{in} = 47\mu F$, $C_{out} = 100\mu F$, $L = 10\mu H$, $T_A = +25^\circ C$, unless specified otherwise.



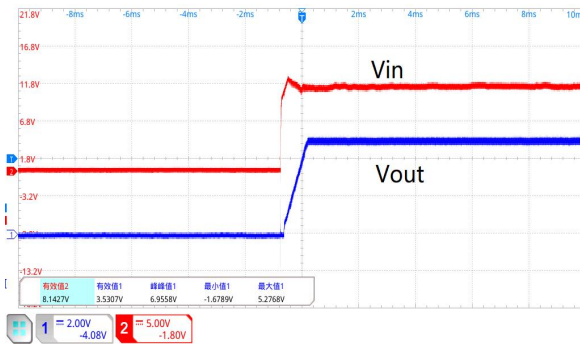
Typical Performance Characteristics



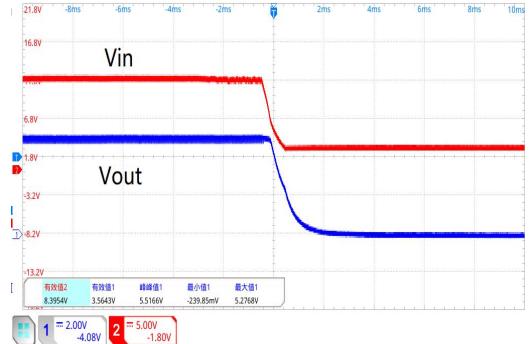
$V_{in}=12V, V_{out}=5V, I_o=NO\ Load$



$V_{in}=12V, V_{out}=5V, I_o=1.2A$

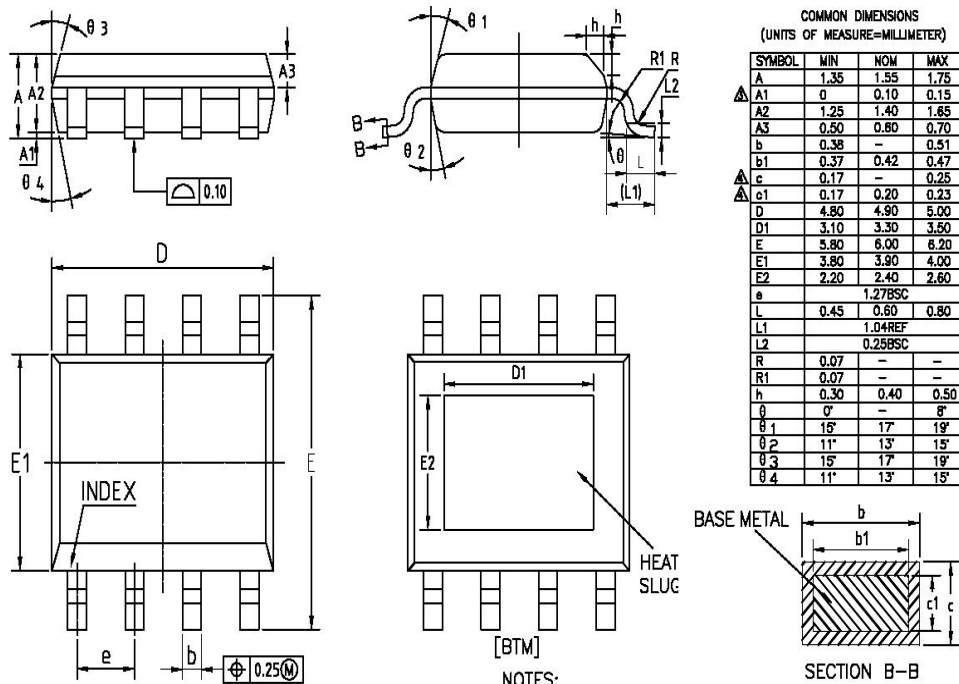


$V_{in}=12V, V_{out}=5V, I_o=1A$ Time (2ms/div)



$V_{in}=12V, V_{out}=5V, I_o=1A$ Time (2ms/div)

Package Information (ESOP-8)



NOTES:

ALL DIMENSIONS REFER TO JEDEC STANDARD MS-012 AA
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	-	-	1.77	D	4.7	4.9	5.1
A1	0.08	0.18	0.28	E	5.8	6	6.2
A2	1.2	1.4	1.6	E1	3.7	3.9	4.1
A3	0.55	0.65	0.75	e	1.27BSC		
b	0.39	-	0.48	L	0.5	0.65	0.8
b1	0.38	0.41	0.43	L1	1.05BSC		
c	0.21	-	0.26	θ	0	-	8°
D1	3.1	3.3	3.5	E2	2.2	2.4	2.6

Special Instructions

The company reserves the right of final interpretation of this specification.

Version Change Description

Version: V1.0	Author: Xinchun Li	Time: 2022.9.9
Modify the record:		
1. Editio princeps		
Version: V1.1	Author: Yang	Time: 2023.5.29
Modify the record:		
1. Update Block Diagram and Pin Arrangement Diagram		

Statement

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